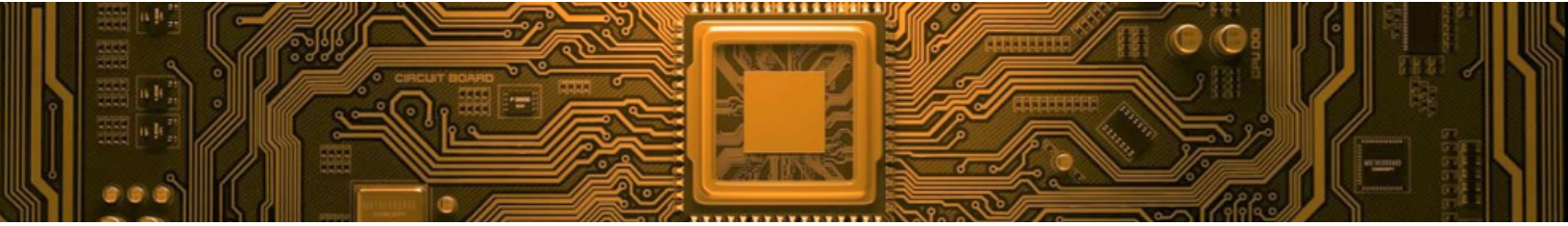


Semiconductors At Purdue and ASML Technical Seminar



Patterning Semiconductor Integrated Circuits with Holistic Nanolithography

Come join Ronald Goossens and Tony Yen of ASML for a technical seminar on the history of photolithography, the patterning technology that has fueled our industry's progress for so many decades. Starting with contact/proximity printing, our industry gradually switched in the mid-1970s to projection lithography whose resolution is governed by the famous formula , moving step-by-step to higher numerical apertures and shorter exposure wavelengths to provide better and better resolution.



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Tony Yen

Vice President
Head of Technology Development Center

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When Thursday, October 26
1:30 PM - 2:30 PM

Where Burton D. Morgan (MRGN 121)

RSVP <https://tinyurl.com/ASML102623>



Registration QR Code



<https://engineering.purdue.edu/semiconductors>
semiconductors@purdue.edu

<https://www.asml.com/en>