



Summer research report from Semiconductors@Birck VIP program

Nora Miqueleiz-Alonso

Chemical Engineering

Hao Tian Pang

Electrical and Computer Engineering



Wednesday, Sept. 17th @ 2:00 pm in BRK 1001

Coffee and snacks served before seminar

also on [MS Teams](#)

About Nora's project: Nora collaborated with Indiana artist Ryan Lane to replicate his artwork into the micron-scale by using different lithography and etching processes. The final design is an array of micron-sized (down to 5 μm diameter) holes etched into a SiO_2 / Si wafer to produce a structural color effect. Nora is a Junior in Chemical Engineering. In addition to her summer job on this VIP project, she has taken the Semiconductors@Birck VIP class for 1 semester and was in the STARS program in summer 2024.

About Hao Tian's project: Hao Tian conducted wafer fabrication at Birck Nanotechnology Center, including exposure, development, metal deposition, and lift-off, while using Statistical Process Control to ensure feature quality and accurate step heights. Hao Tian is a Senior at Purdue, majoring in Electrical Engineering with a concentration in Microelectronics and Semiconductor. In addition to his summer job in SPC, he has taken the Semiconductors@Birck VIP class for 3 semesters.

Host: Neil Dilley, Research Manager | Email: ndilley@purdue.edu