

SEMICONDUCTORS AT PURDUE DISTINGUISHED LECTURE

# *GLOBALFOUNDRIES INNOVATION ROADMAP FOR DIFFERENTIATED ESSENTIAL SEMICONDUCTORS*

WEDNESDAY, JANUARY 15, 9:00-10:00 AM  
MSEE 190, RICE DESIGN STUDIO



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Ted Letavic is a Corporate Fellow and Senior Vice President of Technology Innovation at GlobalFoundries. He is a group leader with technical roadmap responsibility for solution architecture and semiconductor innovation in market segments that include compute and datacenter, wired and wireless infrastructure, mobility, industrial/IoT/ATV, and high speed communications. His recent research interests include silicon photonics, sub tera-Hertz semiconductor devices, analog compute-in-memory, and quantum systems. He has over 60 US patents granted, has authored over 70 reviewed scientific papers, and serves on numerous academic and industrial advisory boards. He received a PhD in Electrical Engineering from Rensselaer Polytechnic Institute.

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