

# Innovation and Risk Taking in Silicon Valley From Research to Product Development



**FRIDAY MARCH 6<sup>th</sup> 3:45-4:45 PM**

**ROOM: Physics Bldg Room 114**

**FREE PIZZA**

**Guest Speaker: Mario Paniccia**

**Purdue Graduate: Ph.D. Physics 1994**

**THIS TALK IS FOR FRESHMAN TO GRAD STUDENTS OF ANY MAJOR WHO WANT TO LEARN HOW INNOVATION AND ENTREPRENEURSHIP REALLY HAPPEN**

## **Abstract:**

The talk will give insights and learnings on the journey of taking Silicon Photonics from a crazy idea to today being one of the key technologies used to transmit optical data in every data center around the world and impacting other areas such AI, Autonomous Vehicles, Bio-sensors and other. Dr Paniccia will talk about the inside story of the successes and also failures along the way to make this technology a reality and give his insights to the next generation of future scientists, innovators, and entrepreneurs as they go into the real world.

## **Bio:**

Dr. Paniccia is a seasoned technical and business executive based in Silicon Valley, CA. In November 2019, Paniccia was appointed CEO and Chairman of Anello Photonics, a VC backed Silicon Photonics startup which he co-founded. Prior to Anello Photonics, Paniccia spent 22 years at Intel Corporation where he achieved the status of Intel Fellow, Chief Technology Officer and GM for the Silicon Photonics Solutions Group. Paniccia started Intel's Silicon Photonics program from its inception and drove the technology to product commercialization. This silicon photonics technology is deployed today in most of the major cloud and data centers around the world. Paniccia is considered one of the industry pioneers and a world expert in the field of silicon photonics.

Paniccia earned a bachelor's degree in physics in 1988 from the State University of New York at Binghamton and a Ph.D. in Solid State Physics from Purdue University in 1994. In October 2008 he was named by R&D magazine as "Scientist of the Year". In May 2009 Paniccia was awarded an Honorary Doctorate Degree from Binghamton University. In 2014 Paniccia completed an accelerated Executive leadership program at Stanford University School of Business. Paniccia has over 100 patents issued or pending. He is a Fellow of the IEEE, SPIE and OSA. In February 2021 Paniccia was inducted into the National Academy of Engineers (NAE) for his "*contributions and commercialization to the field of silicon photonics*".