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INTEL

PhD Tech Talk

Date: Wednesday, February 17th
Time: 5:00pm -6:30pm
Location: BRK 2001

Please join us for a presentation on Leading Edge Silicon Technology Development, hosted by Intel's Jonas Cisneros. Jonas Cisneros is a process engineering manager at Intel Corporation. He has been with Intel's Logic Technology Development Group since 1998. Mr. Cisneros manages the Lithography area in D1D Ramp. D1D Ramp is the organization responsible for taking the latest generation process technology from development into high volume manufacturing. Logic Technology Development (LTD) is within Intel's Technology Manufacturing Group and is where silicon process and device technology is invented and ramped into high volume manufacturing. We are located in Portland, Oregon.

Immediate opportunities for PhD students in the following areas:

Electrical Engineering	Materials	Optics	Computer Engineering
Chemical Engineering	Physics	Chemistry	Computer Science